

TSMC-02-460



April 16, 2004

To: Commissioner for Patents
P.O.Box 1450
Alexandria, VA 22313-1450

Fr: George O. Saile, Reg. No. 19,572
28 Davis Avenue
Poughkeepsie, N.Y. 12603

Subject: | Serial No. 10/810,966 03/26/04 |
Chin-Huang Wang
HIGH SPEED AND LOW POWER SENSE
AMPLIFIER
| _____ |

INFORMATION DISCLOSURE STATEMENT

Enclosed is Form PTO-1449, Information Disclosure Citation
In An Application.

The following Patents and/or Publications are submitted to
comply with the duty of disclosure under CFR 1.97-1.99 and
37 CFR 1.56.

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being
deposited with the United States Postal Service as first class
mail in an envelope addressed to: Commissioner for Patents,
P.O. Box 1450, Alexandria, VA 22313-1450, on April 26, 2004.

Stephen B. Ackerman, Reg.# 37761

Signature/Date

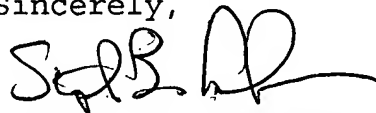
SPB RD 4/26/04

U.S. Patent 6,363,023 to Andersen et al., "Bi-Directional Differential Low Power Sense Amp and Memory System," describes a device and a method which reduced power consumption in memory devices.

U.S. Patent 6,301,179 to Lawson, "Self-Equalized Low Power Precharge Sense Amp for High Speed SRAMs," discloses a memory sense amplifier subsystem which uses complementary PMOS FETs and NMOS FETs.

U.S. Patent 6,249,470 to Andersen et al., "Bi-Directional Differential Low Power Sense Amp and Memory System," discloses a sense amplifier subsystem which utilizes differential data buses which further reduces power consumption while providing high performance.

Sincerely,

A handwritten signature in black ink, appearing to read "SBA", with a long horizontal flourish extending to the right.

Stephen B. Ackerman,
Reg. No. 37761

